

Ic Layout Basics A Practical Guide Mcgraw Hill Professional Engineering

This book presents a framework for the reuse-based design of AMS circuits. The framework is founded on three key elements: (1) a CAD-supported hierarchical design flow; (2) a complete, clear definition of the AMS reusable block; (3) the design for a reusability set of tools, methods, and guidelines. The book features a detailed tutorial and in-depth coverage of all issues and must-have properties of reusable AMS blocks.

Discussing some of the most popular and practical integrated circuits, this book offers projects in which you can learn to create useful and interesting devices with ICs. Provides a real working knowledge of ICs enabling you to design and build your own projects.

Focused on the field of knowledge lying between digital and analog circuit theory, this new text will help engineers working with digital systems shorten their product development cycles and help fix their latest design problems. The scope of the material covered includes signal reflection, crosstalk, and noise problems which occur in high speed digital machines (above 10 megahertz). This volume will be of practical use to digital logic designers, staff and senior communications scientists, and all those interested in digital design.

A practical, engineering book discussing the most modern and general techniques for designing analog integrated circuits which are not digital (excluding

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computer circuits). Covers the basics of the devices, manufacturing technology, design procedures, shortcuts, and analytic techniques. Includes examples and illustrations of the best current practice.

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A practical overview of CMOS circuit design, this book covers the technology, analysis, and design techniques of voltage reference circuits. The design requirements covered follow modern CMOS processes, with an emphasis on low power, low voltage, and low temperature coefficient voltage reference design. Dedicating a chapter to each stage of the design process, the authors have organized the content to give readers the tools they need to implement the technologies themselves. Readers will gain an understanding of device characteristics, the practical considerations behind circuit topology, and potential problems with each type of circuit. Many design examples are used throughout, most of which have been tested with silicon implementation or employed in real-world products. This ensures that the material presented is relevant to both students studying the topic as well as readers requiring a practical viewpoint. Covers CMOS voltage reference circuit design, from the basics through to advanced topics. Provides an overview of basic device physics and different building blocks of voltage reference designs. Features real-world examples based on actual silicon implementation. Includes analytical exercises, simulation exercises, and silicon layout exercises, giving readers guidance and design layout experience for

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voltage reference circuits Solution manual available to instructors from the book's companion website This book is highly useful for graduate students in VLSI design, as well as practicing analog engineers and IC design professionals. Advanced undergraduates preparing for further study in VLSI will also find this book a helpful companion text.

CMOS: Front-End Electronics for Radiation Sensors offers a comprehensive introduction to integrated front-end electronics for radiation detectors, focusing on devices that capture individual particles or photons and are used in nuclear and high energy physics, space instrumentation, medical physics, homeland security, and related fields. Emphasizing practical design and implementation, this book:

- Covers the fundamental principles of signal processing for radiation detectors
- Discusses the relevant analog building blocks used in the front-end electronics
- Employs systematically weak and moderate inversion regimes in circuit analysis
- Makes complex topics such as noise and circuit-weighting functions more accessible
- Includes numerical examples where appropriate

CMOS: Front-End Electronics for Radiation Sensors provides specialized knowledge previously obtained only through the study of multiple technical and scientific papers. It is an ideal text for students of physics and electronics engineering, as well as a useful reference for experienced practitioners.

Advances in Analog and RF IC Design for Wireless Communication Systems gives technical introductions to the latest and most significant topics in the area of circuit design of analog/RF ICs for wireless communication

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systems, emphasizing wireless infrastructure rather than handsets. The book ranges from very high performance circuits for complex wireless infrastructure systems to selected highly integrated systems for handsets and mobile devices. Coverage includes power amplifiers, low-noise amplifiers, modulators, analog-to-digital converters (ADCs) and digital-to-analog converters (DACs), and even single-chip radios. This book offers a quick grasp of emerging research topics in RF integrated circuit design and their potential applications, with brief introductions to key topics followed by references to specialist papers for further reading. All of the chapters, compiled by editors well known in their field, have been authored by renowned experts in the subject. Each includes a complete introduction, followed by the relevant most significant and recent results on the topic at hand. This book gives researchers in industry and universities a quick grasp of the most important developments in analog and RF integrated circuit design. Emerging research topics in RF IC design and its potential application Case studies and practical implementation examples Covers fundamental building blocks of a cellular base station system and satellite infrastructure Insights from the experts on the design and the technology trade-offs, the challenges and open questions they often face References to specialist papers for further reading

MASTER IC LAYOUT WITHOUT AN ENGINEERING BACKGROUND! Tto new chip applications such as cell phones, personal digital assistants, and consumer electronics, electronic

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semiconductor usage has exploded, creating an unprecedented demand for technicians skilled in CMOS and bipolar design and layout. In IC LAYOUT BASICS, you get the same top-notch material utilized in IBM's successful training courses. This essential primer brings you up to speed on: *

Integrated circuit processes * Layout techniques *

Fundamental device concepts * Wafer processes

Writing for technicians without an engineering degree, the authors present concepts from the ground up, building on the simple until the complex becomes crystal clear. Examples, self-tests, and sidebars reinforce the material and make it all quick and painless. For maximum retention, each chapter includes preview points, "motivation" boxes, and executive summaries.

Praise for CMOS: Circuit Design, Layout, and Simulation Revised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State University "This

book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits,

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references, amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS circuits from design to implementation CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage

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of both analog and digital circuit-level design techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning

This book provides pertinent and practical information about how to create, work, and thrive in an Integrated Care (IC) setting. Unlike other books on the subject, it focuses on the "nuts and bolts" of establishing an IC practice; it also covers material that is often missing from or insufficiently covered in the existing literature. Specific topics discussed include the basics of IC, such as different models and levels of IC and examples of IC initiatives; how to build an IC program, with guidelines for entering and working effectively in a practice, as well as managing the associated economic aspects; ethical issues involved in IC, given the discrepancies between medical and mental health ethical standards; assessment and intervention in IC; cross-cultural and diversity issues in IC; and leadership, consultation, and supervision.

The book is organized in seven chapters. Physical design flow. Timing constraints. Place and route concepts. Tool vendors. Process constraints. Timing closure. Place and route methodology and flow.

ECO and spare gates. Formal verification. Coupling noise. Chip optimization and tapeout.

Reliability concerns and the limitations of process technology can sometimes restrict the innovation process involved in designing nano-scale analog circuits. The success of nano-scale analog circuit design requires repeat experimentation, correct analysis of the device physics, process technology, and adequate use of the knowledge database.

Starting with the basics, *Nano-Scale CMOS Analog Circuits: Models and CAD Techniques for High-Level Design* introduces the essential fundamental concepts for designing analog circuits with optimal performances. This book explains the links between the physics and technology of scaled MOS transistors and the design and simulation of nano-scale analog circuits. It also explores the development of structured computer-aided design (CAD) techniques for architecture-level and circuit-level design of analog circuits. The book outlines the general trends of technology scaling with respect to device geometry, process parameters, and supply voltage. It describes models and optimization techniques, as well as the compact modeling of scaled MOS transistors for VLSI circuit simulation. • Includes two learning-based methods: the artificial neural network (ANN) and the least-squares support vector machine (LS-SVM) method • Provides case studies demonstrating the practical use of these two

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methods • Explores circuit sizing and specification translation tasks • Introduces the particle swarm optimization technique and provides examples of sizing analog circuits • Discusses the advanced effects of scaled MOS transistors like narrow width effects, and vertical and lateral channel engineering Nano-Scale CMOS Analog Circuits: Models and CAD Techniques for High-Level Design describes the models and CAD techniques, explores the physics of MOS transistors, and considers the design challenges involving statistical variations of process technology parameters and reliability constraints related to circuit design.

Feeling stuck? Find out how to work toward the career of your dreams If you're slogging through your days in a boring or unrewarding job, it may be time to make a big change. Careers For Dummies is a comprehensive career guide from a top career coach and counselor that will help you jump start your career and your life. Dive in to learn more about career opportunities, with a plethora of job descriptions and the certifications, degrees, and continuing education that can help you build the career you've always wanted. Whether you're entering the workforce for the first time or a career-oriented person who needs or wants a change, this book has valuable information that can help you achieve your career goals. Find out how you can build your personal brand to become more attractive

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to potential employers, how to create a plan to “get from here to there” on your career path, and access videos and checklists that help to drive home all the key points. If you’re not happy in your day-to-day work now, there’s no better time than the present to work towards change. Get inspired by learning about a wide variety of careers Create a path forward for a new or better career that will be rewarding and fun Determine how to build your personal brand to enhance your career opportunities Get tips from a top career coach to help you plan and implement a strategy for a more rewarding work life Careers For Dummies is the complete resource for those looking to enhance their careers or embark on a more rewarding work experience.

The 2nd Edition of Analog Integrated Circuit Design focuses on more coverage about several types of circuits that have increased in importance in the past decade. Furthermore, the text is enhanced with material on CMOS IC device modeling, updated processing layout and expanded coverage to reflect technical innovations. CMOS devices and circuits have more influence in this edition as well as a reduced amount of text on BiCMOS and bipolar information. New chapters include topics on frequency response of analog ICs and basic theory of feedback amplifiers.

Places emphasis on developing intuition and physical insight. This title includes numerous

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examples and problems that have been carefully thought out to promote problem solving methodologies of the type engineers apply daily on the job.

Design state-of-the-art charge pumps Charge Pump IC Design delivers an advanced systematic approach to charge pump circuit design—from building blocks to final pump. The book describes how to achieve high power efficiency and low supply noise. Negative feedback control, compensation, and stability are discussed and real-world design examples with schematics are included. The proven techniques presented in this practical, cutting-edge guide will help you to provide the efficient power conversion needed for today's portable electronic devices. Comprehensive coverage includes:

Regulators and power converters Charge pump design specifications and design metrics Single stage charge pump Multi-stage charge pump Charge pump clock driver Charge pump stability analysis Charge pump design, regulation, and control by examples Charge pump applications

This book covers the fundamental knowledge of layout design from the ground up, addressing both physical design, as generally applied to digital circuits, and analog layout. Such knowledge provides the critical awareness and insights a layout designer must possess to convert a structural description produced during circuit design into the

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physical layout used for IC/PCB fabrication. The book introduces the technological know-how to transform silicon into functional devices, to understand the technology for which a layout is targeted (Chap. 2). Using this core technology knowledge as the foundation, subsequent chapters delve deeper into specific constraints and aspects of physical design, such as interfaces, design rules and libraries (Chap. 3), design flows and models (Chap. 4), design steps (Chap. 5), analog design specifics (Chap. 6), and finally reliability measures (Chap. 7). Besides serving as a textbook for engineering students, this book is a foundational reference for today's circuit designers.

Discover cutting-edge techniques for next-generation integrated circuit design, and learn how to deliver improved speed, density, power, and cost.

Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed.

The book provides a comprehensive overview of electromigration and its effects on the reliability of electronic circuits. It introduces the physical process of electromigration, which gives the reader the requisite understanding and knowledge for adopting appropriate counter measures. A comprehensive set of options is presented for modifying the present IC design methodology to prevent electromigration.

Finally, the authors show how specific effects can be

exploited in present and future technologies to reduce electromigration's negative impact on circuit reliability.

The last decade has brought explosive growth in the technology for manufacturing integrated circuits. Integrated circuits with several hundred thousand transistors are now commonplace. This manufacturing capability, combined with the economic benefits of large electronic systems, is forcing a revolution in the design of these systems and providing a challenge to those people interested in integrated system design. Modern circuits are too complex for an individual to comprehend completely. Managing tremendous complexity and automating the design process have become crucial issues. Two groups are interested in dealing with complexity and in developing algorithms to automate the design process. One group is composed of practitioners in computer-aided design (CAD) who develop computer programs to aid the circuit-design process. The second group is made up of computer scientists and mathematicians who are interested in the design and analysis of efficient combinatorial algorithms. These two groups have developed separate bodies of literature and, until recently, have had relatively little interaction. An obstacle to bringing these two groups together is the lack of books that discuss issues of importance to both groups in the same context. There are many

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instances when a familiarity with the literature of the other group would be beneficial. Some practitioners could use known theoretical results to improve their "cut and try" heuristics. In other cases, theoreticians have published impractical or highly abstracted toy formulations, thinking that the latter are important for circuit layout.

This book includes basic methodologies, review of basic electrical rules and how they apply, design rules, IC planning, detailed checklists for design review, specific layout design flows, specialized block design, interconnect design, and also additional information on design limitations due to production requirements. *Practical, hands-on approach to CMOS layout theory and design *Offers engineers and technicians the training materials they need to stay current in circuit design technology. *Covers manufacturing processes and their effect on layout and design decisions

This comprehensive and insightful book discusses ESD protection circuit design problems from an IC designer's perspective. On-Chip ESD Protection for Integrated Circuits: An IC Design Perspective provides both fundamental and advanced materials needed by a circuit designer for designing ESD protection circuits, including: Testing models and standards adopted by U.S. Department of Defense, EIA/JEDEC, ESD Association, Automotive Electronics Council, International Electrotechnical

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Commission, etc. ESD failure analysis, protection devices, and protection of sub-circuits Whole-chip ESD protection and ESD-to-circuit interactions Advanced low-parasitic compact ESD protection structures for RF and mixed-signal IC's Mixed-mode ESD simulation-design methodologies for design prediction ESD-to-circuit interactions, and more! Many real world ESD protection circuit design examples are provided. The book can be used as a reference book for working IC designers and as a textbook for students in the IC design field.

The "Red Book" presents a background to conventional foundation analysis and design. The text is not intended to replace the much more comprehensive 'standard' textbooks, but rather to support and augment these in a few important areas, supplying methods applicable to practical cases handled daily by practising engineers and providing the basic soil mechanics background to those methods. It concentrates on the static design for stationary foundation conditions. Although the topic is far from exhaustively treated, it does intend to present most of the basic material needed for a practising engineer involved in routine geotechnical design, as well as provide the tools for an engineering student to approach and solve common geotechnical design problems. Analog circuit and system design today is more essential than ever before. With the growth of digital systems, wireless communications, complex industrial and automotive systems, designers are challenged to develop sophisticated analog solutions. This

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comprehensive source book of circuit design solutions will aid systems designers with elegant and practical design techniques that focus on common circuit design challenges. The book's in-depth application examples provide insight into circuit design and application solutions that you can apply in today's demanding designs. Covers the fundamentals of linear/analog circuit and system design to guide engineers with their design challenges Based on the Application Notes of Linear Technology, the foremost designer of high performance analog products, readers will gain practical insights into design techniques and practice Broad range of topics, including power management tutorials, switching regulator design, linear regulator design, data conversion, signal conditioning, and high frequency/RF design Contributors include the leading lights in analog design, Robert Dobkin, Jim Williams and Carl Nelson, among others

Essential reading for experts in the field of RF circuit design and engineers needing a good reference. This book provides complete design procedures for multiple-pole Butterworth, Chebyshev, and Bessel filters. It also covers capacitors, inductors, and other components with their behavior at RF frequencies discussed in detail.

Provides complete design procedures for multiple-pole Butterworth, Chebyshev, and Bessel filters Covers capacitors, inductors, and other components with their behavior at RF frequencies discussed in detail

The latest techniques for designing robust, high performance integrated circuits in nanoscale technologies Focusing on a new technological paradigm,

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this practical guide describes the interconnect-centric design methodologies that are now the major focus of nanoscale integrated circuits (ICs). High Performance Integrated Circuit Design begins by discussing the dominant role of on-chip interconnects and provides an overview of technology scaling. The book goes on to cover data signaling, power management, synchronization, and substrate-aware design. Specific design constraints and methodologies unique to each type of interconnect are addressed. This comprehensive volume also explains the design of specialized circuits such as tapered buffers and repeaters for data signaling, voltage regulators for power management, and phase-locked loops for synchronization. This is an invaluable resource for students, researchers, and engineers working in the area of high performance ICs. Coverage includes:

- Technology scaling
- Interconnect modeling and extraction
- Signal propagation and delay analysis
- Interconnect coupling noise
- Global signaling
- Power generation
- Power distribution networks
- CAD of power networks
- Techniques to reduce power supply noise
- Power dissipation
- Synchronization theory and tradeoffs
- Synchronous system characteristics
- On-chip clock generation and distribution
- Substrate noise in mixed-signal ICs
- Techniques to reduce substrate noise

Integrated Circuit Mask Design teaches integrated circuit (IC) processes, mask design techniques, and fundamental device concepts in everyday language. It develops ideas from the ground up, building complex concepts out of simple ones, constantly reinforcing what has been taught with examples, self-tests and sidebars

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covering the motivation behind the material covered. Accompanying CD-ROM contains a test version of the software for modeling coplanar circuit components and circuits.

This book covers issues and solutions in the physical integration and tapeout management for VLSI design. Chapter 1 gives the overview. Chapter 2 shows detailed techniques for physical design. Chapter 3 provides CAD flows. Chapter 4 discusses on-chip interconnects. A glossary of keywords is provided at the end.

This book enables design engineers to be more effective in designing discrete and integrated circuits by helping them understand the role of analog devices in their circuit design. Analog elements are at the heart of many important functions in both discrete and integrated circuits, but from a design perspective the analog components are often the most difficult to understand. Examples include operational amplifiers, D/A and A/D converters and active filters. Effective circuit design requires a strong understanding of the operation of these analog devices and how they affect circuit design.

Comprehensive coverage of analog circuit components for the practicing engineer Market-validated design information for all major types of linear circuits Includes practical advice on how to read op amp data sheets and how to choose off-the-shelf op amps Full chapter covering printed circuit board design issues

This book teaches the skills and knowledge required by today's RF and microwave engineer in a concise, structured and systematic way. Reflecting modern developments in the field, this book focuses on active

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circuit design covering the latest devices and design techniques. From electromagnetic and transmission line theory and S-parameters through to amplifier and oscillator design, techniques for low noise and broadband design; This book focuses on analysis and design including up to date material on MMIC design techniques. With this book you will: Learn the basics of RF and microwave circuit analysis and design, with an emphasis on active circuits, and become familiar with the operating principles of the most common active system building blocks such as amplifiers, oscillators and mixers Be able to design transistor-based amplifiers, oscillators and mixers by means of basic design methodologies Be able to apply established graphical design tools, such as the Smith chart and feedback mappings, to the design RF and microwave active circuits Acquire a set of basic design skills and useful tools that can be employed without recourse to complex computer aided design Structured in the form of modular chapters, each covering a specific topic in a concise form suitable for delivery in a single lecture Emphasis on clear explanation and a step-by-step approach that aims to help students to easily grasp complex concepts Contains tutorial questions and problems allowing readers to test their knowledge An accompanying website containing supporting material in the form of slides and software (MATLAB) listings Unique material on negative resistance oscillator design, noise analysis and three-port design techniques Covers the latest developments in microwave active circuit design with new approaches that are not covered elsewhere

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Publisher's Note: Products purchased from Third Party sellers are not guaranteed by the publisher for quality, authenticity, or access to any online entitlements included with the product. Learn the principles and practices of simulation-based analog IC design This comprehensive textbook and on-the-job reference offers clear instruction on analog integrated circuit design using the latest simulation techniques. Ideal for graduate students and professionals alike, the book shows, step by step, how to develop and deploy integrated circuits for cutting-edge Internet of Things (IoT) and other applications. Analog Integrated Circuit Design by Simulation: Techniques, Tools, and Methods lays out practical, ready-to-apply engineering strategies.

Application layer, device layer, and circuit layer IC design are covered in complete detail. You will learn how to tackle real-world design problems and avoid long cycles of trial and error. Coverage includes:

- First-order DC response
- Unified closed-loop model
- Accurate modeling of DC response
- Frequency and step response
- Multi-pole dynamic response and stability
- Effect of external network on differential gain
- Continuous-time and discrete-time amplifiers
- MOSFET, NMOS, and PMOS characteristics
- Small-signal modeling and circuit analysis
- Resistor and capacitor design
- Current sources, sinks, and mirrors
- Basic, symmetrical, folded-cascode, and Miller OTAs
- Opamps with source-follower and common-source output stages
- Fully differential OTAs and opamps

The Third Edition of CMOS Circuit Design, Layout, and Simulation continues to cover the practical design of both

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analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks including: phase-locked-loops, delta-sigma sensing circuits, voltage/current references, op-amps, the design of data converters, and much more.

Regardless of one's integrated circuit (IC) design skill level, this book allows readers to experience both the theory behind, and the hands-on implementation of, complementary metal oxide semiconductor (CMOS) IC design via detailed derivations, discussions, and hundreds of design, layout, and simulation examples. Millimeter-Wave Integrated Circuits delivers a detailed overview of MMIC design, specifically focusing on designs for the millimeter-wave (mm-wave) frequency range. The scope of the book is broad, spanning detailed discussions of high-frequency materials and technologies, high-frequency devices, and the design of high-frequency circuits. The design material is supplemented as appropriate by theoretical analyses. The broad scope of the book gives the reader a good theoretical and practical understanding of mm-wave circuit design. It is best-suited for both undergraduate students who are reading or studying high frequency circuit design and postgraduate students who are specializing in the mm-wave field.

Using the book and the software provided with it, the reader can build his/her own tester arrangement to investigate key aspects of analog-, digital- and mixed system circuits. Plan of attack based on traditional testing, circuit design and circuit manufacture allows the reader to appreciate a testing regime from the point of

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view of all the participating interests Worked examples based on theoretical bookwork, practical experimentation and simulation exercises teach the reader how to test circuits thoroughly and effectively

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